

## **400Gb/S QSFP-DD Direct Attach Cable**

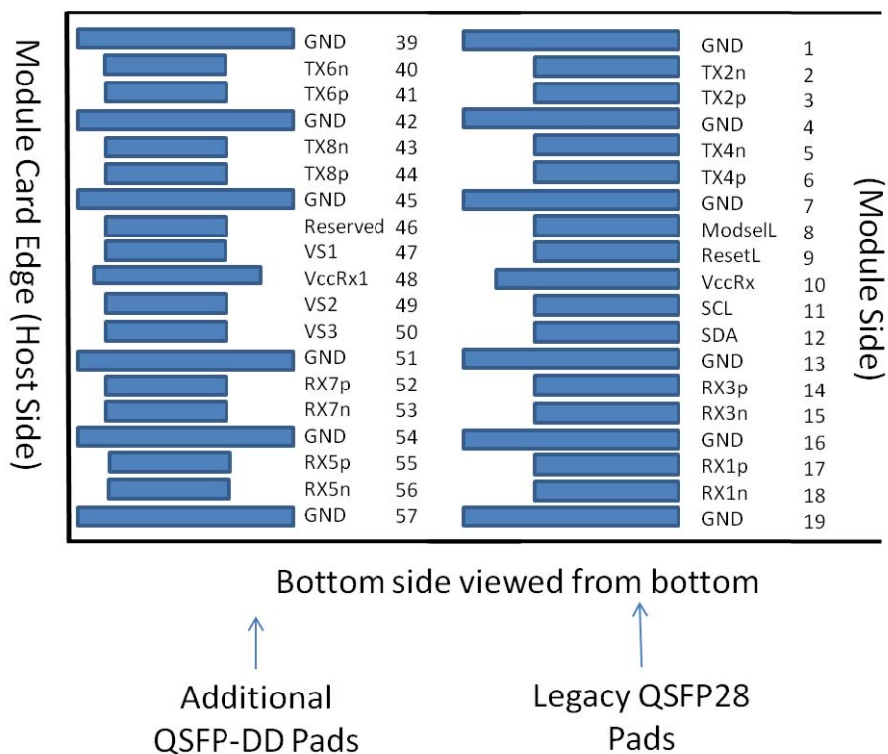
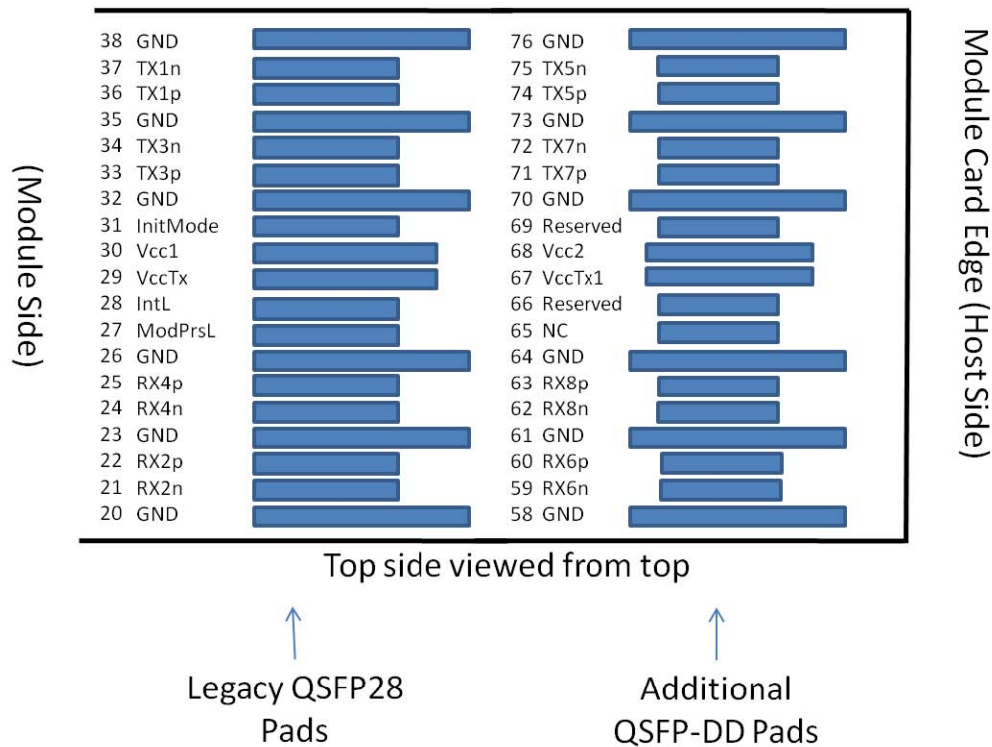
**P/N: AEC-PxxAA0**



### **Product Features**

- Compliant with 400G Ethernet IEEE 802.3bs
- Compliant to QSFP-DD Rev. 2.0
- Supports 400 Gbps data rate links
- Using standard 2 Row by 12 Channel MPO Connector
- Single 3.3V power supply
- 0°C to 70°C case temperature operating range
- Hot pluggable electrical interface
- I2C management interface
- RoHS-6 Compliant

## QSFP28 Module Pad Assignments and Descriptions

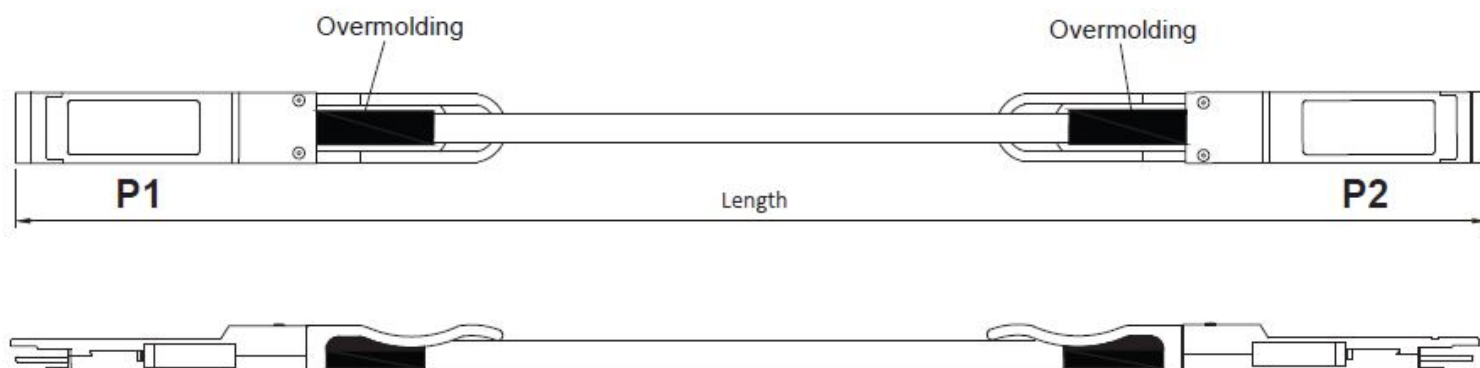


Pin	Logic	Symbol	Description	Plug Sequence	Notes
1		GND	Ground	1B	
2	CML-I	Tx2n	Transmitter Inverted Data Input	3B	
3	CML-I	Tx2p	Transmitter Non-Inverted Data Input	3B	
4		GND	Ground	1B	
5	CML-I	Tx4n	Transmitter Inverted Data Input	3B	
6	CML-I	Tx4p	Transmitter Non-Inverted Data Input	3B	
7		GND	Ground	1B	
8	LVTTL-I	ModSelL	Module Select	3B	
9	LVTTL-I	ResetL	Module Reset	3B	
10		VccRx	+3.3V Power Supply Receiver	2B	
11	LVC MOS-I/O	SCL	2-wire serial interface clock	3B	
12	LVC MOS-I/O	SDA	2-wire serial interface data	3B	
13		GND	Ground	1B	
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	3B	
15	CML-O	Rx3n	Receiver Inverted Data Output	3B	
16		GND	Ground	1B	
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	3B	
18	CML-O	Rx1n	Receiver Inverted Data Output	3B	
19		GND	Ground	1B	
20		GND	Ground	1B	
21	CML-O	Rx2n	Receiver Inverted Data Output	3B	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	3B	
23		GND	Ground	1B	
24	CML-O	Rx4n	Receiver Inverted Data Output	3B	
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	3B	
26		GND	Ground	1B	
27	LVTTL-O	ModPrsL	Module Present	3B	
28	LVTTL-O	IntL	Interrupt	3B	
29		VccTx	+3.3V Power supply transmitter	2B	

30		Vcc1	+3.3V Power supply	2B	
31	LVTTTL-I	InitMode	Initialization mode; In legacy QSFP applications, the InitMode pad is called LPMODE	3B	
32		GND	Ground	1B	
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input	3B	
34	CML-I	Tx3n	Transmitter Inverted Data Input	3B	
35		GND	Ground	1B	
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	3B	
37	CML-I	Tx1n	Transmitter Inverted Data Input	3B	
38		GND	Ground	1B	
39		GND	Ground	1A	
40	CML-I	Tx6n	Transmitter Inverted Data Input	3A	
41	CML-I	Tx6p	Transmitter Non-Inverted Data Input	3A	
42		GND	Ground	1A	
43	CML-I	Tx8n	Transmitter Inverted Data Input	3A	
44	CML-I	Tx8p	Transmitter Non-Inverted Data Input	3A	
45		GND	Ground	1A	
46		Reserved	For future use	3A	
47		VS1	Module Vendor Specific 1	3A	
48		VccRx1	3.3V Power Supply	2A	
49		VS2	Module Vendor Specific 2	3A	
50		VS3	Module Vendor Specific 3	3A	
51		GND	Ground	1A	
52	CML-O	Rx7p	Receiver Non-Inverted Data Output	3A	
53	CML-O	Rx7n	Receiver Inverted Data Output	3A	
54		GND	Ground	1A	
55	CML-O	Rx5p	Receiver Non-Inverted Data Output	3A	
56	CML-O	Rx5n	Receiver Inverted Data Output	3A	
57		GND	Ground	1A	
58		GND	Ground	1A	
59	CML-O	Rx6n	Receiver Inverted Data Output	3A	
60	CML-O	Rx6p	Receiver Non-Inverted Data Output	3A	
61		GND	Ground	1A	

62	CML-O	Rx8n	Receiver Inverted Data Output	3A	
63	CML-O	Rx8p	Receiver Non-Inverted Data Output	3A	
64		GND	Ground	1A	
65		NC	No Connect	3A	
66		Reserved	For future use	3A	
67		VccTx1	3.3V Power Supply	2A	
68		Vcc2	3.3V Power Supply	2A	
69		Reserved	For Future Use	3A	
70		GND	Ground	1A	
71	CML-I	Tx7p	Transmitter Non-Inverted Data Input	3A	
72	CML-I	Tx7n	Transmitter Inverted Data Input	3A	
73		GND	Ground	1A	
74	CML-I	Tx5p	Transmitter Non-Inverted Data Input	3A	
75	CML-I	Tx5n	Transmitter Inverted Data Input	3A	
76		GND	Ground	1A	

## Mechanical Design Diagram



## Order Information

Model Name	Module Type	AWG	Length
AEC-PG0AA0	400Gb/S QSFP-DD Direct Attach Cable	30	0.5M
AEC-PG1AA0	400Gb/S QSFP-DD Direct Attach Cable	30	1M
AEC-PGX15AA0	400Gb/S QSFP-DD Direct Attach Cable	30	1.5M
AEC-PF2AA0	400Gb/S QSFP-DD Direct Attach Cable	28	2M
AEC-PFX25AA0	400Gb/S QSFP-DD Direct Attach Cable	28	2.5M