

1.25Gb/S SFP LX 10km Transceiver

P/N: TSFP-1CxF13A



Product Features

- Compliant with SFP MSA and SFF-8472 with LC receptacle
- Hot-pluggable
- Complies with Telcordia (Bellcore) GR-468-CORE
- Single 3.3V power supply
- SFP mechanical interface
- Digital Diagnostic Monitor Interface
- RoHS-6 Compliant

Application

- Gigabit Ethernet
- Fiber Channel

Absolute Maximum Rating

Parameter	Min	Max	Unit	Note
Storage Temperature	-40	85	°C	
3.3V Power Supply Voltage	-0.5	3.6	V	

Recommended Operating Conditions

Parameter	Min	Typical	Max	Unit	Note
Case Operating Temperature	0		70	°C	TSFP-1C1F13A
	-40		85		TSFP-1C5F13A
Power Supply Voltage	3.135	3.3	3.465	V	
Data Rate		1.25		Gbps	
Supply Current			300	mA	
Link Distance			10	km	

Electrical-Optical Specification

Parameter	Symbol	Min	Typical	Max	Unit	Note
Transmitter						
Center Wavelength	λ	1270	1310	1355	nm	
Spectral Width – RMS	$\Delta\lambda$			4	nm	
Average Launch Optical Power	LOP	-9		-3	dBm	
Extinction Ratio	ER	9			dB	
Rise/Fall time (20%~80%)				260	ps	
Differential Input Voltage		500		2400	mV	
Output Optical Eye		IEEE802.3Z Compliant				

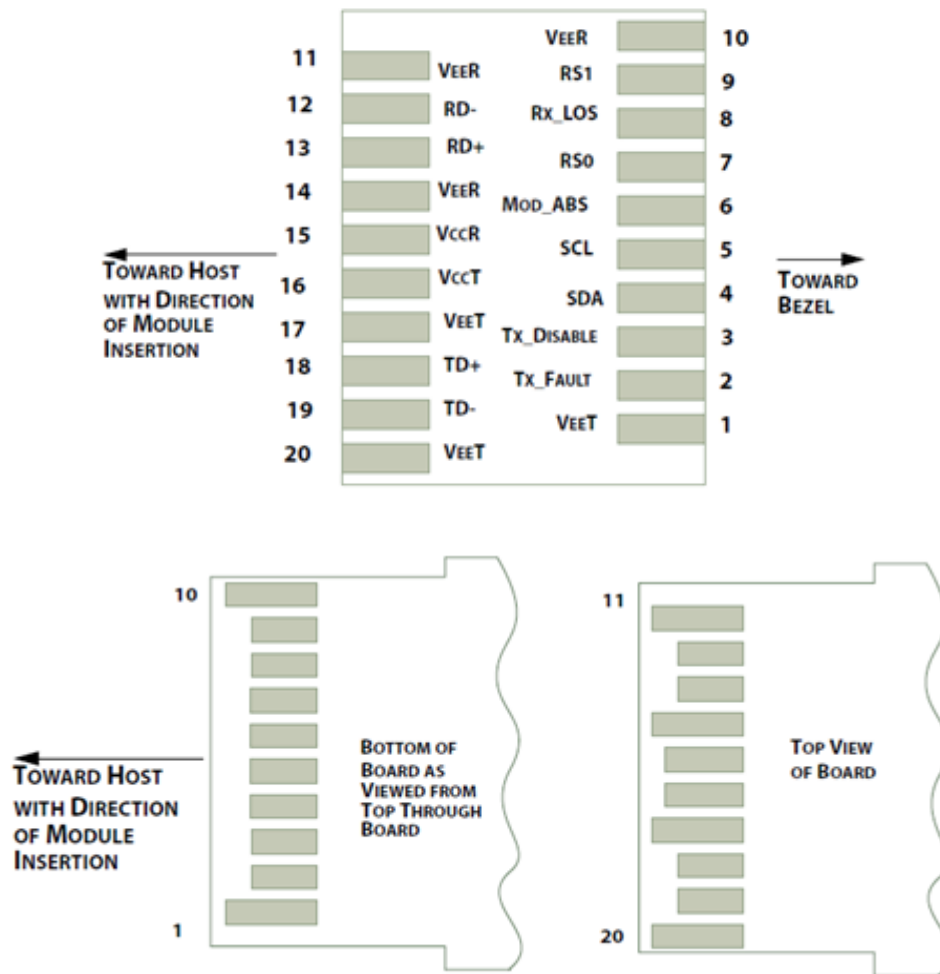
Optical- Electrical Specification

Parameter	Symbol	Min	Typical	Max	Unit	Note
Receiver						
Sensitivity	Psen			-23	dBm	1
Saturation		-3			dBm	1
LOS Assert	LOSA	-45			dBm	
LOS De-assert	LOSD			-32	dBm	
LOS Hysteresis	LOSH	0.5		5	dB	
Differential Data Output Voltage		370		2000	mV	
LOS Output Voltage-Low				0.8	V	
LOS Output Voltage-High		2.0			V	

Notes

1. Minimum Sensitivity and saturation levels for an 8B10B 2⁷-1 PRBS. BER≤10⁻¹²
, 1.25Gbps, ER=9dB

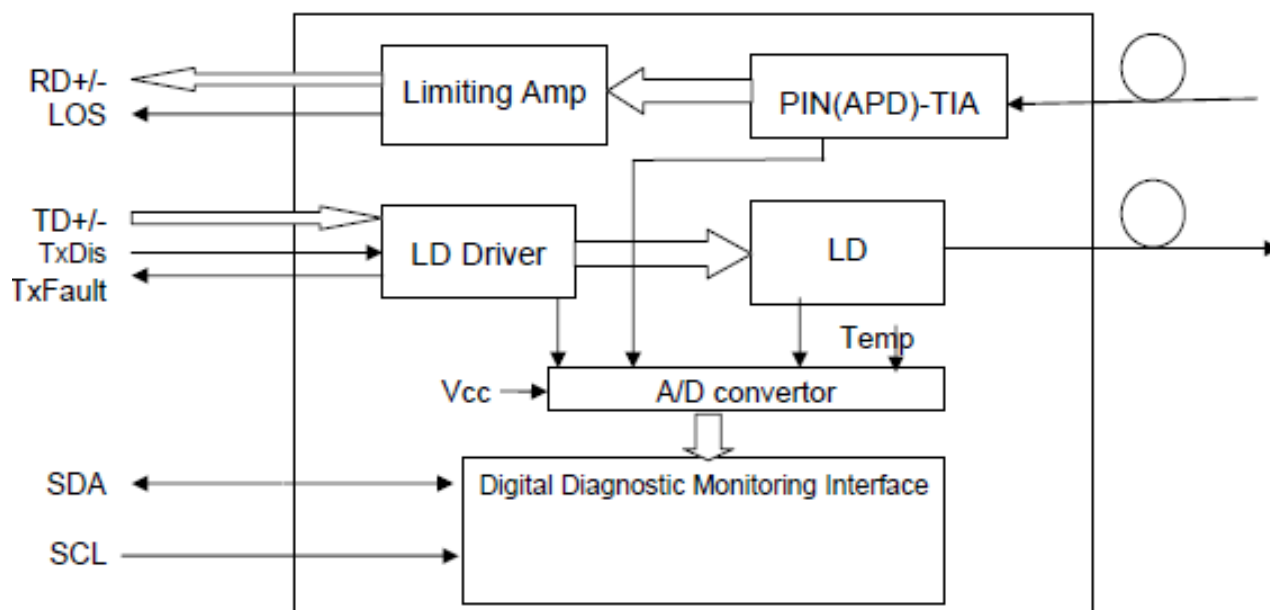
SFP Module Pad Assignments and Descriptions



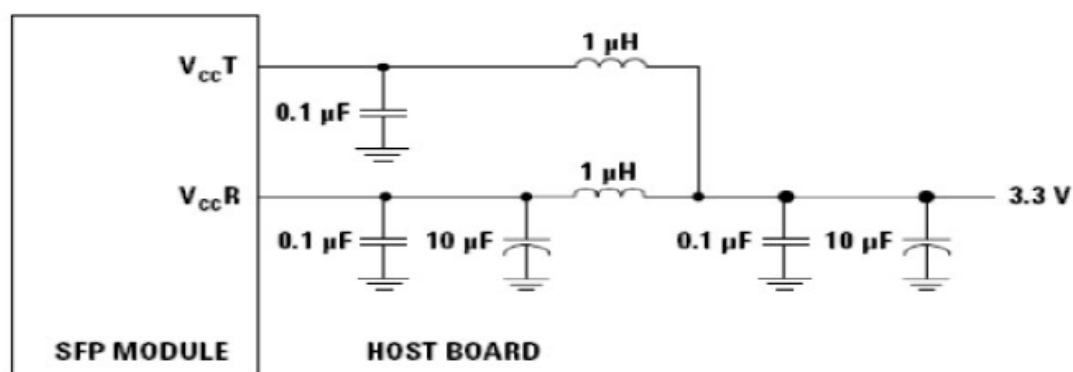
PIN	Logic	Symbol	Name / Description	Note
1		VeeT	Module Transmitter Ground	
2	LVTTL-O	TX_Fault	Module Transmitter Fault	
3	LVTTL-I	TX_Dis	Transmitter Disable; Turns off transmitter laser output	
4	LVTTL-I/O	SDA	2-Wire Serial Interface Data Line	
5	LVTTL-I	SCL	2-Wire Serial Interface Clock	
6		MOD_DEF0	Module Definition, Grounded in the module	
7	LVTTL-I	RS0	Receiver Rate Select, default is high for 8G/10G application, when set to low by system, transceiver will set the bandwidth to under 4.25G to improve the sensitivity at low data rate	

8	LVTTL-O	RX_LOS	Receiver Loss of Signal Indication Active LOW	
9	LVTTL-I	RS1	Transmitter Rate Select, default input is high for 8G/10G application, when set to low by system, transceiver will set the TX optical output to be compliant with low data rate fiber channel specifications	
10		VeeR	Module Receiver Ground	
11		VeeR	Module Receiver Ground	
12	CML-O	RD-	Receiver Inverted Data Output	
13	CML-O	RD+	Receiver Data Output	
14		VeeR	Module Receiver Ground	
15		VccR	Module Receiver 3.3 V Supply	
16		VccT	Module Transmitter 3.3 V Supply	
17		VeeT	Module Transmitter Ground	
18	CML-I	TD+	Transmitter Non-Inverted Data Input	
19	CML-I	TD-	Transmitter Inverted Data Input	
20		VeeT	Module Transmitter Ground	

Transceiver Block Diagram



Recommended Host Board Power Supply Circuit



Mechanical Design Diagram

Unit: mm

